

Ultra Low Gate Charge ($Q_{G(\text{tot})} = 50 \text{ nC}$)

- Low Output Capacitance ($C_{\text{oss}} = 89 \text{ pF}$)
- 100% Avalanche Tested
- AEC- ϵ $^{\circ}\text{C}$

			V_{GSop}	-5/+18	V
Continuous Drain Current (Note 1)	Steady State	$T_{\text{C}} = 25^{\circ}\text{C}$	I_{D}	31	A
Power Dissipation (Note 1)			P_{D}	129	W
Continuous Drain Current (Note 1)	Steady State	$T_{\text{C}} = 100^{\circ}\text{C}$	I_{D}	22	A
Power Dissipation (Note 1)			P_{D}	64	W
Pulsed Drain Current (Note 2)	$T_{\text{C}} = 25^{\circ}\text{C}$		I_{DM}	97	A
Operating Junction and Storage Temperature Range			$T_{\text{J}}, T_{\text{stg}}$	-55 to +175	$^{\circ}\text{C}$
Source Current (Body Diode)			I_{S}	26	A
Single Pulse Drain-to-Source Avalanche Energy ($I_{\text{L(pk)}} = 9.4 \text{ A}$, $L = 1 \text{ mH}$) (Note 3)			E_{AS}	44	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)			T		

NVH4L095N065SC1

THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Max	Unit
Junction to Case Steady State (Note 1)	R_{JC}	1.16	$^{\circ}\text{C}/\text{W}$
Junction to Ambient Steady State (Note 1)	R_{JA}	Note 1)	

(T_J = 25°C unless otherwise specified) (continued)

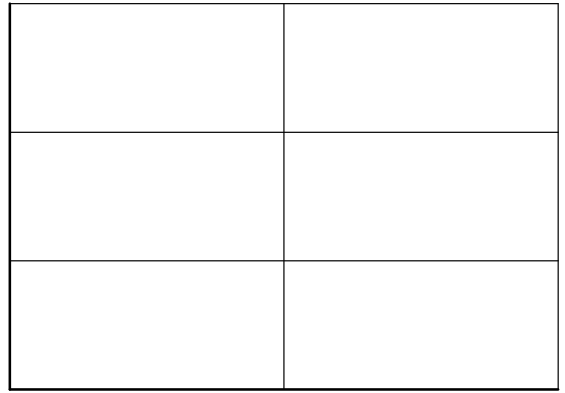
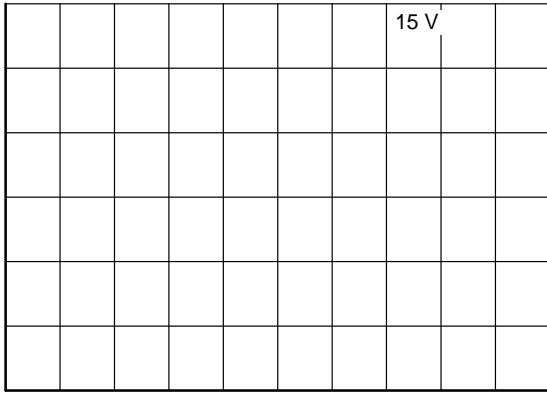
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Reverse Recovery Time	t _{RR}	V _{GS} = -5/18 V, I _{SD} = 12 A, dI _S /dt = 1000 A/μs	-	15	-	ns
Reverse Recovery Charge	Q _{RR}		-	62	-	nC
Reverse Recovery Energy	E _{REC}		-	6.5	-	μJ
Peak Reverse Recovery Current	I _{RRM}		-	8	-	A
Charge time	T _a		-	8	-	ns
Discharge time	T _b		-	7	-	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

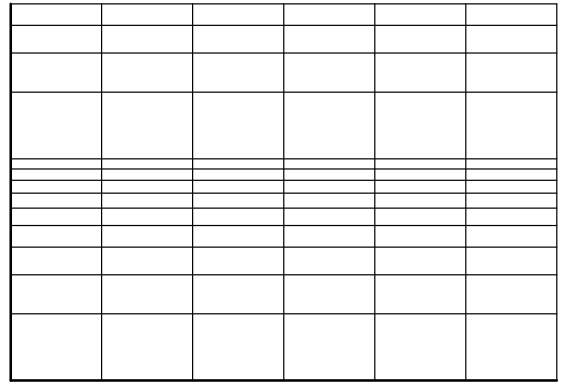
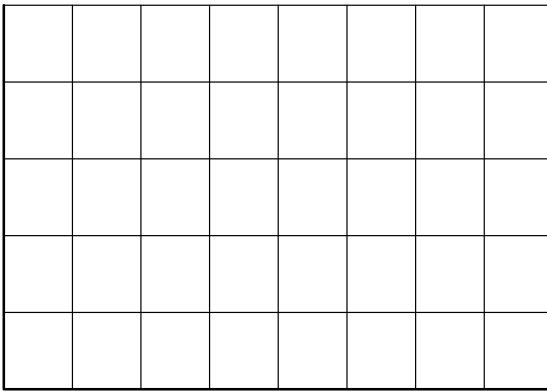
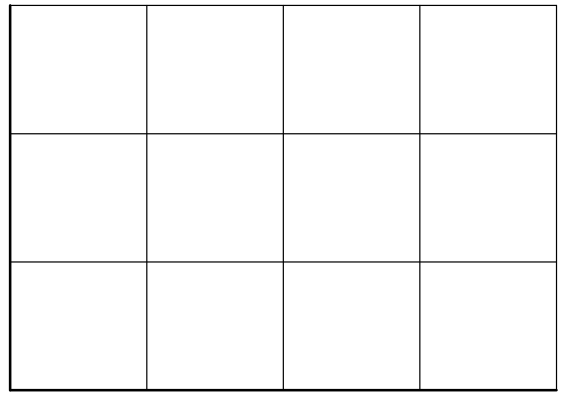
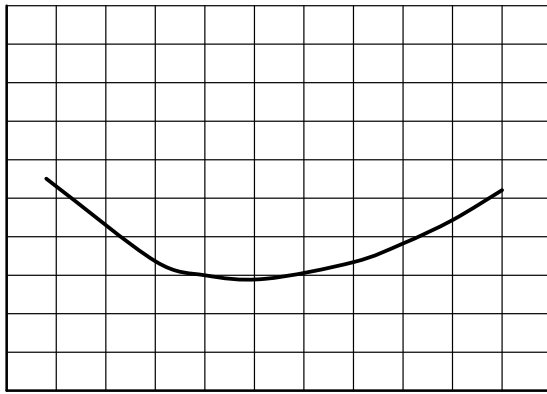




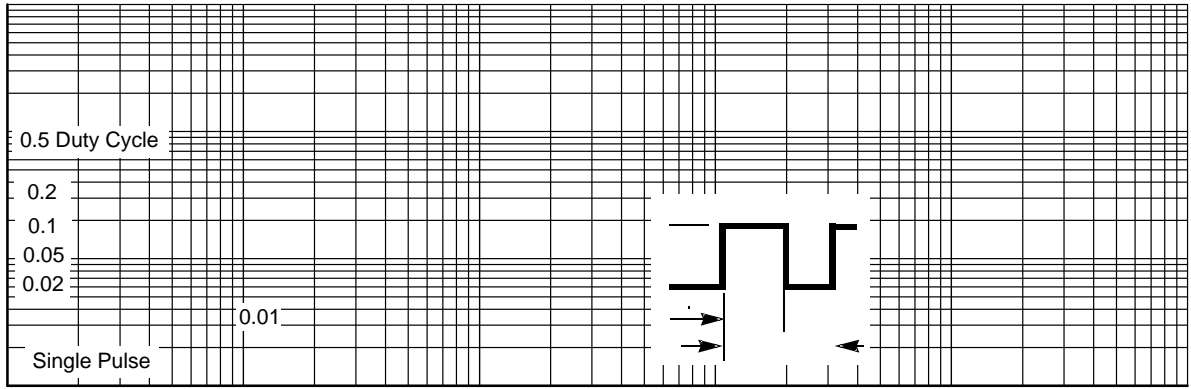
$V_{DS, DRAIN}$ -

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$Z_{\theta JC}(t)$. EFFECTIVE TRANSIENT
THERMAL RESISTANCE ($^{\circ}\text{C}/\text{W}$)



t , RECTANGULAR PULSE DURATION (sec)

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