

## Product Preview

The NXH015F120M3F1PTG is a power module containing 15 mΩ/1200 V SiC MOSFET full bridge and a thermistor with Al<sub>2</sub>O<sub>3</sub> DBC in an F1 package.

### Features

- 15 mΩ / 1200 V M3S SiC MOSFET Full Bridge
- Al<sub>2</sub>O<sub>3</sub> DBC
- Thermistor
- Options with Pre Applied Thermal Interface Material (TIM) and without Pre Applied TIM
- Press Fit Pins
- These Devices are Pb Free, Halide Free and are RoHS Compliant

### Typical Applications

- Solar Inverter
- Uninterruptible Power Supplies
- Electric Vehicle Charging Stations
- Industrial Power

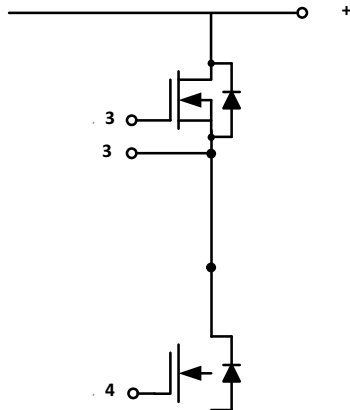


Figure 1. NXH015F120M3F1PTG Schematic Diagram

# NXH015F120M3F1PTG

## PIN FUNCTION DESCRIPTION

Pin	Name	Description
1	AC2	Center point of full bridge 2
2	AC2	

# NXH015F120M3F1PTG

## RECOMMENDED OPERATING RANGES

Rating	Symbol	Min	Max	Unit
Module Operating Junction Temperature	$T_J$	-40	150	$^{\circ}\text{C}$

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

## ELECTRICAL CHARACTERISTICS ( $T_J = 25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
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### SiC MOSFET CHARACTERISTICS

Zero Gate Voltage Drain Current	$V_{GS} = 0\text{ V}, V_{DS} = 1200\text{ V}, T_J = 25^{\circ}\text{C}$	$I_{DSS}$	-	-	200	$\mu\text{A}$
Drain-Source On Resistance	$V_{GS} = 18\text{ V}, I_D = 60\text{ A}, T_J = 25^{\circ}\text{C}$	$R_{DS(ON)}$	-	14.8	19	$\text{m}\Omega$
	$V_{GS} = 18\text{ V}, I_D = 60\text{ A}, T_J = 125^{\circ}\text{C}$		-	24.7	-	
	$V_{GS} = 18\text{ V}, I_D = 60\text{ A}, T_J = 150^{\circ}\text{C}$		-	28.7	-	
	$V_{GS} = 18\text{ V}, I_D = 60\text{ A}, T_J = 175^{\circ}\text{C}$		-	33	-	
Gate-Source Threshold Voltage	$V_{GS} = V_{DS}, I_D = 30\text{ mA}$	$V_{GS(TH)}$	2.04	2.4	4.4	V
Recommended Gate Voltage		$V_{GOP}$	-3	-	+18	V
Gate-to-Source Leakage Current	$V_{GS} = +22/-10\text{ V}, V_{DS} = 0\text{ V}$	$I_{GSS}$	-	-	$\pm 2$	$\mu\text{A}$
Input Capacitance	$V_{GS} = 0\text{ V}, f = 1\text{ MHz}, V_{DS} = 800\text{ V}$	$C_{ISS}$	-	4696	-	$\text{pF}$
Reverse Transfer Capacitance		$C_{RSS}$	-	20.1	-	
Output Capacitance		$C_{OSS}$	-	287	-	
Total Gate Charge	$V_{GS} = -3/18\text{ V}, V_{DS} = 800\text{ V}, I_D = 30\text{ A}$	$Q_{G(TOTAL)}$	-	211	-	nC
Gate-Source Charge		$Q_{GS}$	-	16	-	
Gate-Drain Charge		$Q_{GD}$	-	50	-	
		$Q_{GINT}$	-	1.65	-	
		$t_{d(on)}$	-	33.3	-	ns
Turn-off Delay Time		$t_r$	-	8.6	-	

# NXH015F120M3F1PTG

## ELECTRICAL CHARACTERISTICS (continued)(T<sub>J</sub> = 25 °C unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
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### THERMISTOR CHARACTERISTICS

Power Dissipation – Absolute Maximum	5 mA			34.2		mW
Power Dissipation Constant			–	1.4	–	mW/K
B–value	B(25/50), tolerance ±2%		–	3375	–	K
B–value	B(25/100), tolerance ±2%		–	3436	–	K

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

### ORDERING INFORMATION

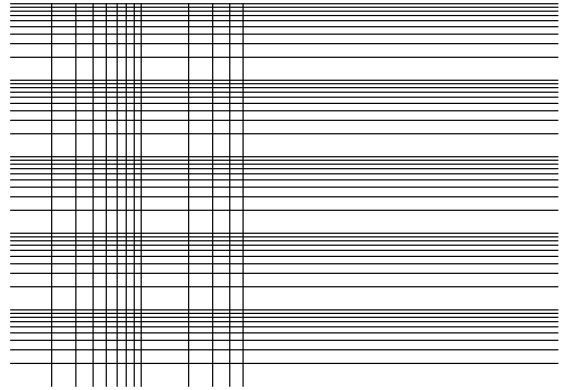
Orderable Part Number	Marking	Package	Shipping
NXH015F120M3F1PTG			



# NXH015F120M3F1PTG

## TYPICAL CHARACTERISTICS

M1, M2, M3, M4 SIC MOSFET CHARACTERISTIC



# NXH015F120M3F1PTG

## TYPICAL CHARACTERISTICS

M1, M2, M3, M4 SIC MOSFET CHARACTERISTIC







Table 1. CAUER NETWORKS

Cauer Element #	Rth (K/W)	Cth (Ws/K)
1	0.0004413	0.0013801
2	0.0029539	0.0003074
3	88 60.00295393	

88 60.002953975.55115.30754 326AUE9 .90709 15.30745F1P5071 refBT8 0 0 8 450.8786

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