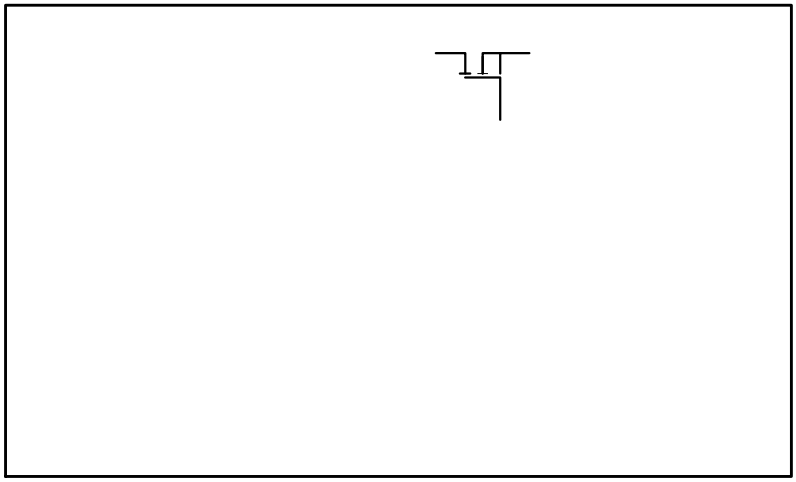
LDO R a 1 A, U a L D , CMOS, B a Ra

Product Preview

T30LMPSR131, T30LAPSR131

The T30LxPSR131 is a 1 A LDO equipped with an NMOS pass transistor and a separate bias supply voltage (V

Publication Order Number:



*Active output discharge function is present only in “A” and “C” option devices.

(−40 °C ≤ T_J ≤ 125 °C; V_{BIAS} = 2.7 V or (V_{OUT} + 1.6 V), whichever is greater, V_{IN} = $V_{OUT(NOM)}$ + 0.1 V, I_{OUT} = 1 mA, V_{EN} = 1 V, C_{IN} = 4.7 μF, C_{OUT} = 10 μF, C_{BIAS} = 1 μF, unless otherwise noted.
Typical values are at T_J = +25 °C. Min/Max values are for −40 °C ≤ T_J ≤ 125 °C unless otherwise noted. (Note 4))

Operating Input Voltage Range		V_{IN}	V_{OUT} + V_{DO}		2.2	V
Operating Bias Voltage Range		V_{BIAS}	(V_{OUT} +			

(continued)
(−40 °C ≤ T_J ≤ 125 °C; V_{BIAS} = 2.7 V or (V_{OUT} + 1.6 V), whichever is greater, V_{IN} = $V_{OUT(NOM)}$ + 0.1 V, I_{OUT} = 1 mA, V_{EN} = 1 V, C_{IN} = 4.7 μF, C_{OUT} = 10 μF, C_{BIAS} = 1 μF, unless otherwise noted.
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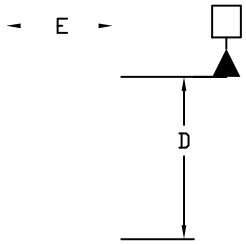

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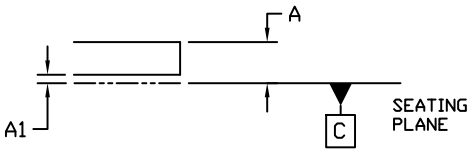
3. COPLANARITY APPLIES TO THE SPHERICAL



SIDE VIEW

DIM	MILLIMETERS	
	MIN.	NOM.
A	---	---
A1	0.04	0.06
A2		
D		
e	0.400 BSC	

BOTTOM VIEW

P AND TOI