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Silicon Carbide (SiC) Cascode JFET -EliteSiC, Power N-Channel, TO-220-3L, 650 V, 80 mohm

DATASHEET

UJ3C065080T3S



Part Number	Package	Marking
UJ3C065080T3S	TO-220-3L	UJ3C065080T3S



Rev. E, Janauary 2025

Description

H\]gG]7 : 9HXYj]W]gVUgYXcbUi b]ei Y WgWcXY WfW]h configuration, in which a normally-on SiC JFET is co-packaged with a Si A C G: 9Hhc dfcXi W Ubcfa U`mcZZG]7 : 9HXYj]W "'H\Y XYj]W g ghUbXUfX [UhY!Xf]j Y WUfUWfY]gh]WgU`ck gZcfUhfi Y`Xfcd!]b fYd`UWa Ybh hc G] = 6HgžG]: 9HgžG]7 A C G: 9Hgcf G]gi dYf t bWf]cb devices. Available in the TO-220-3L package, this device exhibits ultralow gate charge and exceptional reverse recovery characteristics, making it ideal for switching inductive loads , and any application requiring standard gate drive.

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Maximum Ratings

Parameter	Symbol	Test Conditions	Value	Units
Drain-source voltage	V_{DS}		650	V
Gate-source voltage	V_{GS}	DC	-25 to +25	V
Continuous drain current ¹	I	$T_{\rm C} = 25 ^{\circ}{\rm C}$	31	А
	ID	$T_{\rm C} = 100^{\circ}{\rm C}$	23	А
Pulsed drain current ²	I _{DM}	$T_{\rm C} = 25 ^{\circ}{\rm C}$	65	А
Single pulsed avalanche energy ³	E _{AS}	L=15mH, I _{AS} =2.1A	33	mJ
Power dissipation	P _{tot}	$T_{\rm C} = 25 ^{\circ}{\rm C}$	190	W
Maximum junction temperature	T _{J,max}		175	°C
Operating and storage temperature	T _J , T _{STG}		-55 to 175	°C
Max. lead temperature for soldering, %#, Zfca WlgY Zcf) gYWtbXg	TL		250	°C

1. Limited by $T_{\text{J,max}}$

2. Pulse width t_p limited by $T_{J,max}$

3. Starting $T_J = 25^{\circ}C$

Thermal Characteristics

Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
	R _q			0.61	0.79	°C/W











Typical Performance - Dynamic

Min Typ Max

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Typical Performance Diagrams

Figure 1. Typical output characteristics at $T_J = -55$ °C, tp < 250ms

Figure 2. Typical output characteristics at $T_{\rm J}$ = 25°C, tp < 250ms

Figure 3. Typical output characteristics at $T_J = 175$ °C, tp < 250ms

Figure 4. Normalized on-resistance vs. temperature at V_{GS} = 12V and I_{D} = 20A

















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Figure 13. Typical capacitances at f = 100kHz and V_{GS} Figure 14. DC drain current derating = 0V







TO-220-3L PACKAGE OUTLINE, PART MARKING AND TUBE SPECIFICATIONS

PART MARKING

PACKING TYPE

ANTI-STATIC TUBE

QUANTITY /TUBE : 50 UNITS

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