INPUT SECTION

Ethernet Connectors, Magnetics and Termination

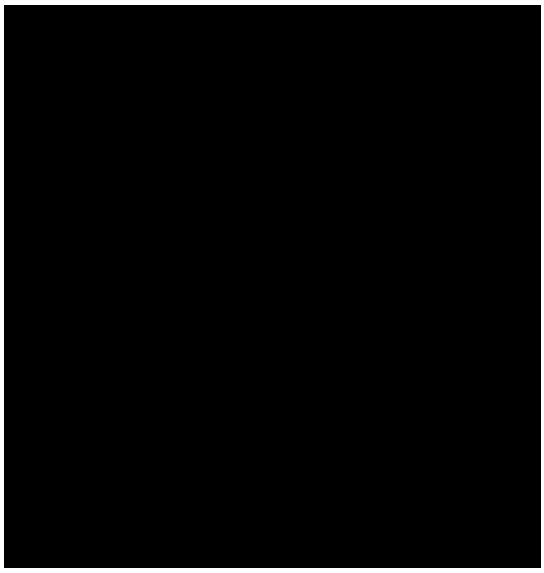

Figure 3. Connection Diagram

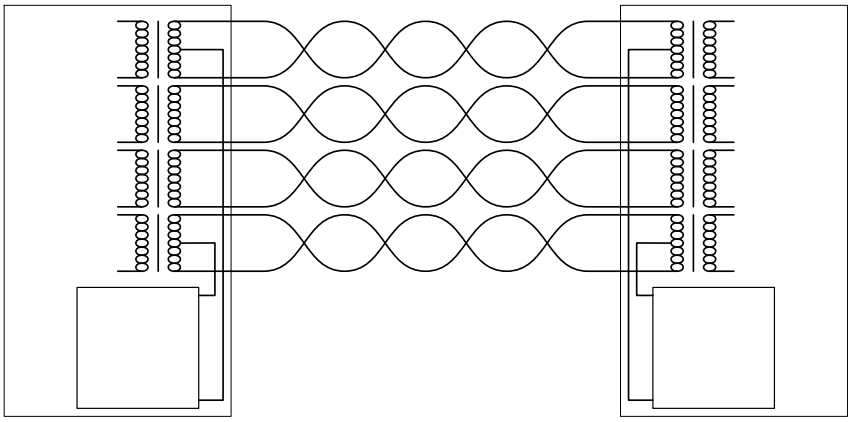

Figure 4. Ethernet Connection with Power−over−Ethernet

NCP109x OPERATION

Power−over−Ethernet Detection and Classification

Ω

Ω Ω

Ω

$$(R_{UVLO1} + R_{UVLO2}) // R_{det1} = 24.9\ k\Omega$$

μ

Table 2. PD POWER CLASSIFICATION

Power Class	Average Input Power of the PD	Classification Resistor
		Ω
		Ω
		Ω
		Ω
		Ω

Inrush and Operational Current Limitation

Table 3. PD INPUT CURRENT LIMITS

Average Input Power of the PD	Recommended ILIM Resistor	Inrush Current Limit	Operational Current Limit
	Ω		
	Ω		

PGOOD Indication

$$\text{uvlo} \qquad \frac{\text{ilvlo} + R}{R_{\text{2vlo}}}$$

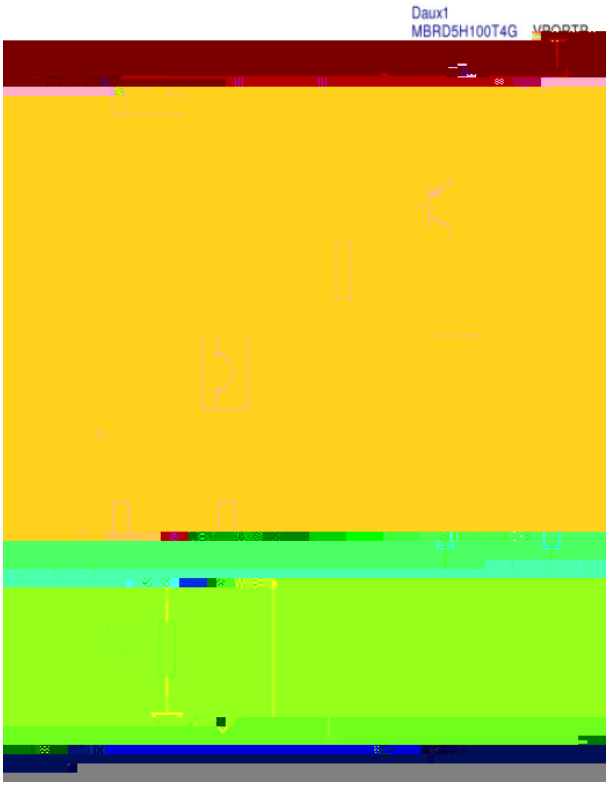

Figure 9. Auxiliary Supply Circuit

nClass_AT

onsemi tion arising out of or associated with any unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of any products and/or the board.

This evaluation board/kit does not fall within the scovwathis equipment may cause interference with radio
e

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