

6

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1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

D

D

C

C

B

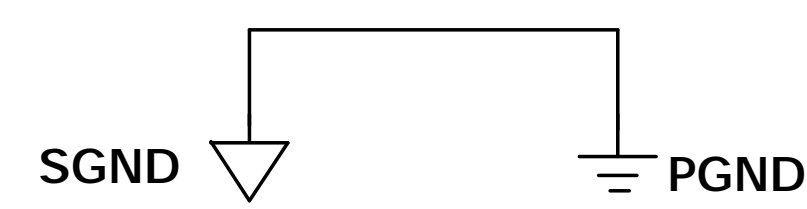
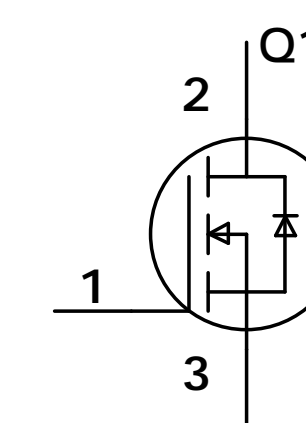
B

A

A

ISOLATION BOUNDARY

XGND
 XVDD
 XGND
 IN+
 XEN
 IN-



NOTE: Make single point connection
from SGND to PGND on PCB

COMPANY: ON SEMICONDUCTOR

TITLE: NCP15705 SiC DRIVER BOARD STANDARD

DRAWN: S. Mappus

DATED: 04/05/19

SIZE: C DRAWING NO: REV: 1.1

SCALE: 1:1 SHEET: 1 OF 2

REVISION RECORD			
		APPROVED:	

COMPANY: **ON SEMICONDUCTOR**

TITLE: **NCP15705 SiC DRIVER BOARD STANDARD**

DRAWN: S. Mappus	DATED: 04/05/19
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

CODE:	SIZE: C	DRAWING NO:	REV: 1.1
SCALE: 1:1			SHEET 2 OF 2