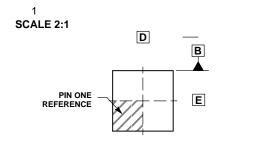
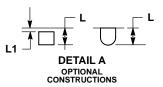
DFN8, 4x4, 0.8P CASE 506CY ISSUE O

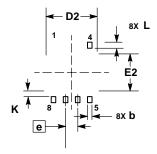
DATE 31 JUL 2014





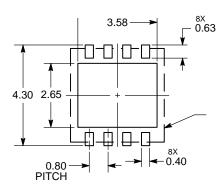






NOTE 3

RECOMMENDED



*For additional information on our Pb–Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

- NOTES:
 1. DIMENSIONS AND TOLERANCING PER
 ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION & APPLIES TO PLATED
 TERMINAL AND IS MEASURED BETWEEN
 0.15 AND 0.30MM FROM TERMINAL TIP.
 4. COPLANARITY APPLIES TO THE EXPOSED
 PAD AS WELL AS THE TERMINALS.

	MILLIMETERS	
DIM	MIN	MAX
Α	0.80	1.00
A1	0.00	0.05
A3	0.20 REF	
b	0.25	0.35
D	4.00 BSC	
D2	3.28	3.48
Е	4.00 BSC	
E2	2.35	2.55
е	0.80 BSC	
K		
L	0.30	0.50

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code = Assembly Location

Α = Wafer Lot L Υ = Year W = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.