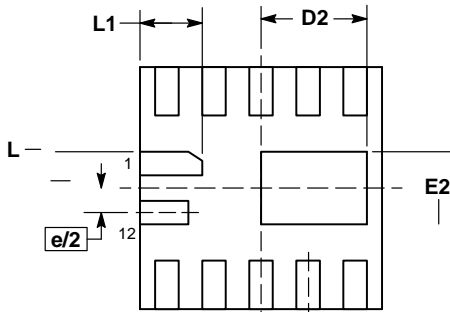
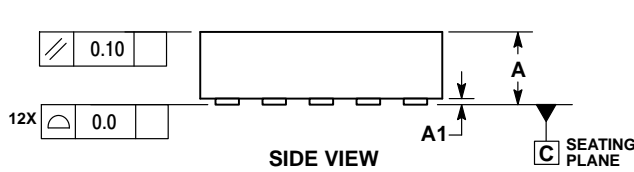
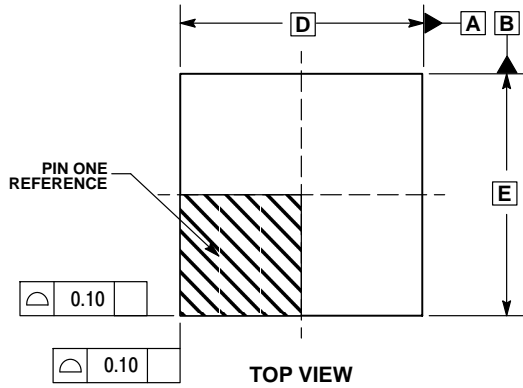




SCALE 4:1

LLGA12, 2x2  
CASE 513AA  
ISSUE A

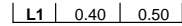
DATE 02 FEB 2015



E	2.00 BSC
E2	0.75
e	0.40 BSC
K	0.25
L	0.30 0.50

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994 .
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

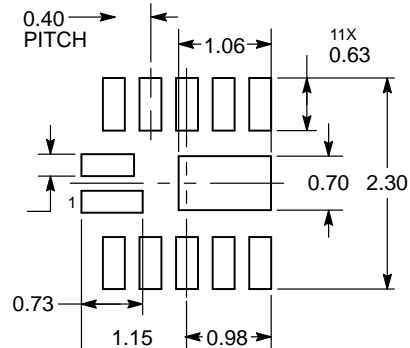


**GENERIC MARKING DIAGRAM\***

XX = Specific Device Code  
M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

**SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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<b>DESCRIPTION:</b>	<b>LLGA12, 2X2, 0.4P</b>	<b>PAGE 1 OF 1</b>

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