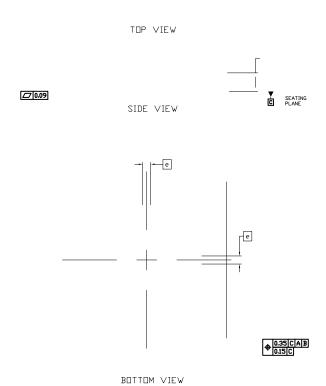
## CLGA163 20.88x19.9, 1P CASE 621AB ISSUE A

## DATE 11 SEP 2018

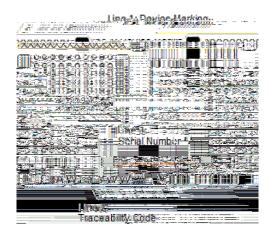


MILLIMETERS		
DIM	MIN.	
Α		
A2	0.91	
k	0.55	
D		

## CLGA163 20.88x19.9, 1P CASE 621AB ISSUE A

**DATE 11 SEP 2018** 

## GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code

A = Assembly Location

WL = Wafer Lot

YY = Year

WW = Work Week

NN = Serial Number

\*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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