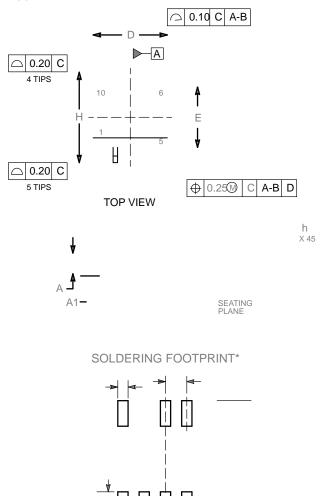
9

1 SCALE 1:1

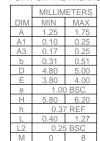




*For additional information on our Pb ïFree strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES:

- NOTES:
 DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.10mm TOTAL IN EXCESS OF 'b' AT MAXIMUM MATERIAL CONDITION.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15mm PER SIDE. DIMENSIONS D AND E ARE DE-TERMINED AT DATUM F.
 DIMENSIONS A AND B ARE TO BE DETERM-
- 5.
- I ERMINED AL DALUM F. DIMENSIONS A AND B ARE TO BE DETERM-INED AT DATUM F. AL IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY. 6.



GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code А

- = Assembly Location
 - = Wafer Lot
- = Year W

L Y

= Work Week

= Pb ïFree Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb ïFree indicator, "G^{''}, may or not be present.