

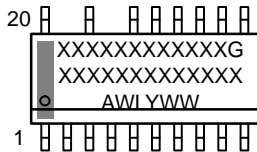
**SOIC20 NB LESS PIN 17 & 19**  
**CASE 751EZ**  
**ISSUE O**

DATE 23 SEP 2019

<b>b</b>	<b>0.31</b>	<b>0.41</b>
<b>-</b>	<b>9.90</b>	
<b>E</b>		

**RECOMMENDED  
MOUNTING FOOTPRINT**

**GENERIC  
MARKING DIAGRAM\***



- XXXXX = Specific Device Code
- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.