



ANLVLSHFT0400

#### Overview

The asicNorth ANLVLSHFT0400 is a for lriving the signal off chip.

his circuit is particularly useful for off-chip pplications where relatively fast negative supply lifferential signaling is required.

## **Applications**

Negative supply off-chip differential signaling Off-chip gate drivers

#### **Features**

Temperature Range: -55C to125C Power Supply: 1.8V +/- 10% Input Range: 1.8V +/- 10% Output Range: 15V +/- 10%

Output Propagation Delay: < 20ns (2pf)

Process: ONC18

Area =  $0.1 \text{ mm}^2 (420 \mu \text{m x } 240 \mu \text{m})$ 

### **Deliverables**

Specification

GDS

CDL

LEF

Verilog model

Application documentation

# **Block Diagram**

