



Description

The PLL QP RH is a Radiation Hardened by Design (RHBD) Phase Locked Loop available in ON Semiconductor ONC18 triple well process technology. ONC18 provides 180nm 1.8V CMOS with a number of variants to support analog, mixed-signal and dual gate oxide. RHBD addresses both Total Ionizing Dose (TID) and Single Event Latch-Up (SEL). The PLL is a general purpose clock source offering a wide range of user programmable output clock frequencies.

The PLL QP RH utilizes a single reference clock and produces two output clocks as shown in the functional block diagram below. The single output digital clock is a user programmable